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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/046,446 01/16/2002 *OK*

** FOREIGN APPLICATIONS *****

JAPAN 2001-010821 01/18/2001 *OK*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/04/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 51	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Quang H. Tran</i> Examiner's Signature	<i>OK</i> Initials			

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TITLE

Semiconductor device and manufacturing method of that

☐ All Fees☐ 1.16 Fees (Filing)